

IN THE SPECIFICATION:

N.D. Please replace the paragraph on page ⁴2, starting with the words "FIG. 5 shows" with the following paragraph, wherein insertions are indicated by underlined text and deletions by strikethroughs:

X

FIG. 5 shows the block diagram of said carrier tracking subsystem 104 of FIG. 1. The M-ary Differential Phase Shift Keying ("MDPSK") signal 109 is received and split into two symmetric paths, the positive one 511 and the negative one 512. The MDPSK signal 511 is down-converted at the down-converter 501 into base-band I- and Q-path signals 513 and 514, which are supplied to the X-correlator 503, where they are correlated with the local PN-sequence. The output 517 of the X-correlator 503 is fed to the VCO loop 505 as the positive control signal. Signal 517 also acts as said control signal 112 for said synchronization subsystem 103 of FIG. 1. The negative path executes the same operations except that the MDPSK signal 512 is down-converted by the orthogonal carriers 522, the frequency of which is different from that of the positive path. The down-converter 502 supplies the base-band I- and Q-path signal 515 and 516 to the X-correlator 504. The signal 518 obtained from the X-correlator 504 is fed to the VCO loop 505 as the negative control signal. The VCO loop provides a clock signal 519 to the frequency generator 506, which generates three different frequencies, the adjusted carrier frequency 520, the frequency 521 with a positive offset to the frequency 520 and the frequency 522 with the same offset but negative to the frequency 520. The frequency 520 is used as the control signal 113 for the data demodulation subsystem 105 of FIG. 1.

Please replace the original abstract with the abstract below, which has been corrected in accordance with the requirements of the Office Action with insertions indicated by underlined text and deletions by strikethroughs to read as follows:

